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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10006860	FILING DATE 12/04/2001	CLASS 326711	SUBCLASS 156	GAU 288 2819	EXAMINER S. Thomas
<b>**APPLICANTS:</b> Miyamoto Yuichiro; Masuno Takashi; Kuroda Gouki;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2000-370,543 12/05/2000					
PG-PUB DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials		PATS-3296US			
TITLE : Asynchronous FIFO circuit and method of reading and writing data through asynchronous FIFO circuit					
U.S. DEPT. OF COM. PAT. & TRADEMARK OFF. 10-101					

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<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner		<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner		<b>DRAWING</b>	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>		Application Examiner	
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